

<b>Form 1449 (Modified)</b>  <b>Information Disclosure Statement By Applicant</b>  (Use Several Sheets if Necessary)	<b>Atty Docket No.</b>	ALTRP092/ A01035
	<b>Application No.:</b>	10/804,294
	<b>Applicant</b>	ESPOSITO, Benjamin J. et al.
	<b>Filing Date</b>	March 18, 2004
	<b>Group</b>	2193
	<b>Conf. No.</b>	7802
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#### U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	1.	5,117,349	05/1992	Tirfing et al.	707	3	
	2.	5,546,477	08/1996	Knowles et al.	382	242	
	3.	6,570,842	05/2003	Landolsi	370	210	
	4.	7,110,927	09/2006	San et al.			1/2001

#### Other Documents

Examiner Initial	No.	Author, Title, Place (e.g. Journal) of Publication, Date
	5.	USPTO Office Action mailed July 12, 2005 in related US Application No. 09/773,853 (17 Pages)
	6.	USPTO Office Action mailed January 4, 2006 in related US Application No. 09/773,853 (12 Pages)
	7.	USPTO Notice of Allowance and Fee(s) Due mailed May 18, 2006 in related US Application No. 09/773,853 (10 Pages)
	8.	"The Altera Advantage News and Views" May 1997. pgs 1-31.
	9.	"Altera News & Views" First Quarter February 2000 pg. 1-44
	10.	Baicher et al., "Learning About Digital Signal Processing Using Spreadsheets and Simulation Software" 1996 pgs 41-48.
	11.	Bishop, R-H., "Modern Control Systems Analysis and Design Using MATLAB & SIMULINK" 1997. pgs. 95-101, 192-207, 211-214.
	12.	Eminoglu et al., "A CAD environment for digital filters using a VerilogHDL based functional bit-serial compiler" 1998. 10 <sup>th</sup> International Conference Monastir Tunisia (abstract). Pgs 1-2.
	13.	"FIR Compiler MegaCore" version 2.6.0. 1995-2003 pg 1-5
	14.	"FIR Compiler MegaCore Function" Solution Brief 41, June 1999, ver. 1 pgs 1-4.
	15.	Hussein et al., "Design and Verification Techniques Used in a Graduate Level VHDL Course". IEEE Frontiers in Education Conference Nov. 10-13 1999 pg. 28-31.
	16.	Mathworks Inc., "Student Edition of MATLAB" 1997. pgs 38,39,256,257,335-345
	17.	Mintzer, Les, "Digital Filtering in FPGA's", 1994 Conference Record of the Twenty-Eighth Asilomar Conference on Signals, Systems and Computers. Vol. 2, pgs. 1373-1377.

<b>Examiner</b>	<b>Date Considered</b>
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Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.